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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/629,631	07/30/2003	Kouichi Anno	501.42963X00	9122	
20457	7590 03/01/2006	EXAMINER			
ANTONELLI, TERRY, STOUT & KRAUS, LLP 1300 NORTH SEVENTEENTH STREET SUITE 1800			TON, MINE	TON, MINH TOAN T	
			ART UNIT	PAPER NUMBER	
	I, VA 22209-3873		2871		

DATE MAILED: 03/01/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application No.	Applicant(s)	
		10/629,631	ANNO ET AL.	
	Office Action Summary	Examiner	Art Unit	·
		Toan Ton	2871	_
Period fe	The MAILING DATE of this communication app or Reply	ears on the cover sheet with the c	orrespondence address	
WHIC - Exte after - If NC - Failu Any	HORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DA ensions of time may be available under the provisions of 37 CFR 1.13 r SIX (6) MONTHS from the mailing date of this communication. O period for reply is specified above, the maximum statutory period we ure to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing ned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tin will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).	
Status				
-	This action is <b>FINAL</b> . 2b) This Since this application is in condition for allowar	action is non-final. nce except for formal matters, pro		
	closed in accordance with the practice under E	х рапе Quayle, 1935 С.D. 11, 4:	53 O.G. 213.	
Disposit	ion of Claims			
5) <u>□</u> 6)⊠	Claim(s) 1-3 and 8-14 is/are pending in the approximation (s) is/are withdraw Claim(s) is/are allowed.  Claim(s) 1-3, 8-14 is/are rejected.  Claim(s) is/are objected to.  Claim(s) are subject to restriction and/or	vn from consideration.		
Applicat	ion Papers			•
10)	The specification is objected to by the Examiner The drawing(s) filed on is/are: a) acce Applicant may not request that any objection to the o Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Ex	epted or b) objected to by the Idrawing(s) be held in abeyance. See ion is required if the drawing(s) is obj	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d)	).
Priority (	under 35 U.S.C. § 119	•		
a)	Acknowledgment is made of a claim for foreign  All b) Some * c) None of:  1. Certified copies of the priority documents  2. Certified copies of the priority documents  3. Copies of the certified copies of the priority application from the International Bureau  See the attached detailed Office action for a list of	s have been received. s have been received in Applicati ity documents have been receive I (PCT Rule 17.2(a)).	on No ed in this National Stage	
2) 🔲 Notic 3) 🔲 Infon	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) er No(s)/Mail Date	4)  Interview Summary Paper No(s)/Mail Da 5)  Notice of Informal P 6)  Other:		

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## Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-3 and 8-14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's admitted prior art (APA hereinafter) in view of Murai et al (US 6862059).

APA discloses a common transflective LCD device comprising (see at least the description of the related art, e.g., col. 1, [0009]): a substrate, a first pixel electrode formed of a light transmissive conductive layer formed in one optical transmissive region which is formed by partitioning the pixel region; a second pixel electrode formed of a non-light transmissive conductive film is formed on the other light reflective region; the first pixel electrode positioned as a lower layer with respect to an insulation film and a hole is formed in the insulation film in a region corresponding to the light transmissive region so as to expose the first pixel electrode, and the second pixel electrode is formed on a light reflective region of the insulation film.

APA fails to disclose 'at least a portion corresponding to a side wall surface of the hole formed in the insulation film is arranged in relation to a light shielding film'.

Murai discloses a transflective LCD device comprising a light shielding means disposed on one of a pair of substrates and at least arranged in relation/at least overlapping with at least a portion corresponding to a side wall surface of the hole formed an the insulation film (see at least Figures 4-5). The use of a light shielding means is common and known in the art for

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yielding advantages such as minimizing light leakage → good contrast. Therefore, it would have been obvious to one of ordinary skill in the art to employ a light shielding means arranged at least in relation with at least a portion corresponding to a side wall surface of the hole formed an the insulation film for advantages such as minimizing light leakage → good contrast.

Materials such as metal are common and known in the art for bus lines, light shielding means. Forming the shielding means and the bus (gate/data) lines of the same material is also common and known in the art for advantages such as cost-reduction. Therefore, it would have been at least obvious to one of ordinary skill in the art to employ the light shielding means and the gate lines made of the same material, as common and known in the art, for advantages such as cost-reduction.

Murai also discloses the contact hole formed close to the switching element (TFT).

The light shielding means disposed one of the pair of substrates is common and known as at least art-recognized functionally equivalent to the light shielding means disposed on another of the pair of substrates. Further, disposing between below the second pixel electrode would further achieve advantages such as protecting the TFT from damaging. Therefore, it would have been at least obvious to one of ordinary skill in the art to form the light shielding means adjacent to the switching element (TFT) (i.e., below the second pixel electrode) for achieving further advantages such as protecting the TFT from damaging.

Response to Arguments

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3. Applicant's arguments filed 12/27/05 have been fully considered but they are not persuasive.

Applicant contended that a certified English translation of the foreign document of the present invention would be filed to overcome the outstanding rejection (remarks dated 12/27/05). However, as of 02/27/06, no certified English translation of the foreign document has been received. Thus, the outstanding rejection is maintained.

## Conclusion

4. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

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## **Contact Information**

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Toan Ton whose telephone number is (571) 272-2303

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

February 27, 2006

TOANTON TOANTER